



GAU 2185
7/10/01
JAH

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

MARC DURANTON

PHF 99,540

Serial No. 09/316,560

GAU 2185

Filed: May 24, 1999

Ex. R. BRAGDON

DATA PROCESSING ARRANGMENT AND MEMORY SYSTEM

RECEIVED

JUL 10 2001

Technology Center 2100

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

AMENDMENT

Sir:

In response to the Office Action dated April 4, 2001,
please amend the above-identified application as follows.

IN THE CLAIMS

Please amend Claims 1-5 as follows:

- Sub ~~1~~
- AI
1. (As Amended) A data processing arrangement comprising:
 - a first processor for providing successive sets of input data;
 - a second processor for receiving successive sets of output data;
 - a memory system comprising a plurality of memory circuits for receiving the successive sets of input data and providing the